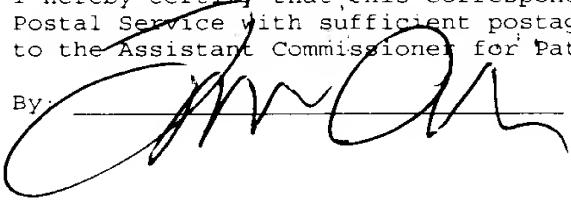


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App'd 5/13/03
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231

By: 

Date: April 28, 2003

5/13/03

*John Smith
for
4/28/03*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Achim Gratz

Applic. No. : 09/940,001

Filed : August 27, 2001

Title : Vertical Non-Volatile Semiconductor Memory Cell and Method for Manufacturing the Memory Cell

Examiner : Richard A. Booth

Group Art Unit : 2812

AMENDMENT under 37 C.F.R. § 1.116

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Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

TECH. 2812
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Sir:

Responsive to the final Office action dated January 27, 2003

kindly amend the above-identified application as follows:

In the Claims:

B1
Claim 1(twice amended). A vertical non-volatile semiconductor memory cell, comprising: